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**UTILITY  
PATENT APPLICATION  
TRANSMITTAL**

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No. 35.C14298

First Named Inventor or Application Identifier

HIRAKU KOZUKA

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**APPLICATION ELEMENTS**

See MPEP chapter 600 concerning utility patent application contents.

**ADDRESS TO:**Assistant Commissioner for Patents  
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1. ☐ Fee Transmittal Form  
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2. ☒ Specification Total Pages
3. ☒ Drawing(s) (35 USC 113) Total Sheets
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- a. ☐ Newly executed (original or copy)
- b. ☒ Unexecuted for information purposes
- c. ☐ Copy from a prior application (37 CFR 1.63(d))  
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- i. ☐ **DELETION OF INVENTOR(S)**  
Signed Statement attached deleting  
inventor(s) named in the prior application, see  
37 CFR 1.63(d)(2) and 1.33(b)
5. ☐ Incorporation By Reference (useable if Box 4c is checked)  
The entire disclosure of the prior application, from which a copy of  
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6. ☐ Microfiche Computer Program (Appendix)
7. Nucleotide and/or Amino Acid Sequence Submission  
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- a. ☐ Computer Readable Copy
- b. ☐ Paper Copy (identical to computer copy)
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**ACCOMPANYING APPLICATION PARTS**

8. ☐ Assignment Papers (cover sheet & document(s))
9. ☐ 37 CFR 3.73(b) Statement ☐ Power of Attorney  
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10. ☐ English Translation Document (if applicable)
11. ☐ Information Disclosure Statement (IDS)/PTO-1449 ☐ Copies of IDS Citations
12. ☐ Preliminary Amendment
13. ☒ Return Receipt Postcard (MPEP 503)  
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14. ☐ Small Entity Statement(s) ☐ Statement filed in prior application  
Status still proper and desired
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17. If a CONTINUING APPLICATION, check appropriate box and supply the requisite information:

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CLAIMS	(1) FOR	(2) NUMBER FILED	(3) NUMBER EXTRA	(4) RATE	(5) CALCULATIONS
	TOTAL CLAIMS (37 CFR 1.16(c))	19-20	0	X \$ 18.00 =	0
	INDEPENDENT CLAIMS (37 CFR 1.16(b))	1-3 =	0	X \$ 78.00 =	0
	MULTIPLE DEPENDENT CLAIMS (if applicable) (37 CFR 1.16(d))			\$260.00 =	\$260.00
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	Reduction by 50% for filing by small entity (Note 37 CFR 1.9, 1.27, 1.28).				
	TOTAL =				\$950.00

19. Small entity status

- a. ☐ A Small entity statement is enclosed
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- c. ☐ Is no longer claimed.

20. ☒ A check in the amount of \$950.00 to cover the filing fee is enclosed.

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- a. ☒ Fees required under 37 CFR 1.16.
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**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT REQUIRED**

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SIGNATURE

DATE

February 16, 2000

/PAP

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PHOTOELECTRIC CONVERSION DEVICE, AND IMAGE SENSOR AND  
IMAGE INPUT SYSTEM MAKING USE OF THE SAME

BACKGROUND OF THE INVENTION

5 Field of the Invention

This invention relates to an image scanner, a facsimile transmission system and other image input systems, or image sensors of various types, and a photoelectric conversion device (or photoelectric  
10 transducer) used in them. More particularly, it relates to an improvement of pixel (picture element) structure.

Related Background Art

Photoelectric conversion devices are widely used  
15 in the above image input systems and image sensors.

As generally known examples, CCD image sensors making use of a reduction optical system and non-reduction contact type image sensors multiple-mounted with a plurality of semiconductor  
20 photosensor chips are used in, e.g., image scanners for inputting images. These are being developed energetically.

As photodetectors used as photoelectric conversion devices in these systems and sensors, it is common to  
25 use photodiodes comprising a semiconductor p-n junction. For example, as disclosed in Japanese Patent Application Laid-Open No. 55-159784, a structure is

proposed in which a substrate having no p-n junction  
formed therein is provided, on its surface, with a  
region having the same conductivity type as the  
substrate and also having a higher impurity density  
5 than the substrate so that the dark current may less  
occur on the substrate surface. Also, as  
photodetectors for one-dimensional photoelectric  
conversion devices, proposed in variety are, e.g., a  
photodetector whose junction capacitance formed by p-n  
10 junction has been made small as disclosed in Japanese  
Patent Application Laid-Open No. 61-264758 and a  
photodetector in which the dark current ascribable to  
the scribed area at chip ends has been designed to less  
occur as disclosed in Japanese Patent Application  
15 Laid-Open No. 1-303752.

Meanwhile, as disclosed in, e.g., Japanese Patent  
Application Laid-Open No. 9-205588, an amplification  
type photoelectric conversion device is proposed in  
which a photodiode is used as a photodetector and  
20 electric charges of this photodetector are full read by  
using a source follower amplifier.

In the case of the above amplification type  
photoelectric conversion device, its light output  $V_p$  is  
expressed by Equation (1).

25 
$$V_p = Q_p / C_o \times G \quad (1)$$

where  $Q_p$  is a quantity of electric charges accumulated  
at the p-n junction,  $C_o$  is a capacitance at the

photoelectric conversion area, G is an amplification degree determined by source follower gain, capacitance-dividing gain, amplifying gain and so forth.

5           In the case of an amplification type photoelectric conversion device having, e.g., a photodiode, an MOS source follower transistor and a reset MOS transistor, this capacitance Co at the photoelectric conversion area can be expressed by:

10                   
$$Co = Cpd + Ca \qquad (2)$$

where Cpd is a p-n junction capacitance of a p-n photodiode, and Ca is other capacitance connected to the photoelectric conversion area, which includes, in the above case, source/well junction capacitance of the  
15   reset MOS transistor, source/gate overlap capacitance, wiring capacitance and the like.

Accordingly, in order to achieve a high sensitivity, it is important to accumulate photoelectric carriers (carriers formed upon exposure  
20   to light) effectively and make as small as possible the capacitance at the photoelectric conversion area at which the carriers are accumulated.

In general, the reset noise produced when a photodetector is reset increases with a decrease in  
25   capacitance at the photoelectric conversion area. However, as disclosed in the above Japanese Patent Application Laid-Open No. 9-205588, the reset noise can

be eliminated by providing a means for retaining noise signals occurring immediately after resetting, and subtracting noise signals from light signals.

However, in such a photoelectric conversion device  
5 in which the noise signals occurring immediately after the resetting of a photodetector are retained during accumulation and subtracted from light signals to eliminate the reset noise of the photodetector, some external noises radiated from the outside of the  
10 photoelectric conversion device, in particular, low-frequency noises such as power-source noise may lower image quality.

Now, where the capacitance at the photoelectric conversion area is represented by  $C_0$ , the amplification  
15 degree by  $G$  and the quantity of photoelectric charges accumulated at the photoelectric conversion area during accumulation by  $Q_P$ , noise output  $V_N$  immediately after the resetting of a photodetector, the quantity of noise charges by  $Q_N$  and light output  $V_S$  after accumulation are  
20 expressed by:

$$V_N = (\sqrt{kTC_0}/C_0) \times G \equiv (Q_N/C_0) \times G \quad (3) \text{ and}$$

$$V_S = ((Q_N + Q_P)/C_0) \times G \quad (4),$$

respectively, and the subtraction made between  
Equations (3) and (4) gives the following equation as  
25 light signals  $V_P$ .

$$\begin{aligned} V_P = V_S - V_N &= ((Q_P + Q_N - Q_N)/C_0) \times G \\ &= (Q_P/C_0) \times G \quad (5) \end{aligned}$$

However, once, e.g., a low-frequency noise of about 50 Hz radiated from a power source of an instrument has mixed into a photodetector, it becomes impossible to eliminate the noise by the subtraction. Especially in the case of the above prior art, the timing at which the noise signals and light signals for which the subtraction is to be made has deviated by the time substantially corresponding to the time of accumulation and also the node of the photodetector during accumulation has come to have a very high impedance in the state of floating, and hence this external noise brings about a very great influence.

Now, where external-noise voltage present in the photodetector at a time ( $t_1$ ) where the noise signals occurring immediately after resetting are read is represented by  $V_{N1}$ , and external-noise electric charges present in the photodetector at a time ( $t_2$ ) where the light output is read after accumulation by  $V_{N2}$ , light signals  $V_p'$  after the subtraction corresponding to the above Equation (5) is as follows:

$$V_p' = ((Q_p/C_0) + V_{N2} - V_{N1}) \times G \quad (6).$$

Also when an external noise having an amplitude  $V_{NR}$  and a frequency  $f_0$  (Hz) is radiated to the photodetector and as long as the noise voltage in the node of the photodetector is  $\Delta V_{NR}$ , the noise voltage at the photodetector node at an arbitrary time  $t$  is expressed by:

$$\Delta V_{NR}(t) = \Delta V_{NR} \times \sin(2\pi f_0 \times t) \quad (7).$$

Now, where the amplitude of external noise in the photodetector,  $\Delta V_{NR}$ , is set at  $2 \pm 1$  (an arbitrary unit), the frequency ( $f_0$ ) of the external noise at 50 Hz and the accumulation time of the photoelectric conversion device at 2.5 msec;

- noise signal read time:  $t_n$  (msec);
  - external noise at the time  $t_n$ :  $V_{N1}$ ;
  - time at the reading of light signals:  $t_s$  (msec);
  - external noise at the time  $t_s$ :  $V_{N2}$ ; and
  - subtraction of external noise:  $\Delta V_N (= V_{N2} - V_{N1})$
- at each field are given as shown in Table 1.



Table 1

	Field number	$t_n$	$V_{N1}$	$t_s$	$V_{N2}$	$\Delta V_N$
5	1	0	0	2.5	0.707	0.707
	2	2.5	0.707	5.0	1.0	0.293
	3	5.0	1.0	7.5	0.707	-0.293
	4	7.5	0.707	10.0	0	-0.707
	5	10.0	0	12.5	-0.707	-0.707
10	6	12.5	-0.707	15.0	-1.0	-0.293
	7	15.0	-1.0	17.5	-0.707	0.293
	8	17.5	-0.707	20.0	0	0.707
	9	20.0	0	22.5	0.707	0.707
	10	22.5	0.707	25.0	1.0	0.293
15	11	25.0	1.0	27.5	0.707	-0.293
	12	27.5	0.707	30.0	0	-0.707
	13	30.0	0	32.5	-0.707	-0.707
	14	32.5	-0.707	35.0	-1	-0.293

20

As can be seen from the above table, mutual interferences between noise frequency and accumulation time cause periodic changes in output after subtraction.

25

Hence, in the case where e.g., all pixels of a photoelectric conversion device are read simultaneously, the above changes in output after the subtraction correspond to changes in the amount of

offset in the direction of secondary scanning. Stated specifically, they appear as periodic stripelike light and shade in images thus read.

5 SUMMARY OF THE INVENTION

An object of the present invention is to provide a photoelectric conversion device that may hardly be affected with radiated noises, in particular, a low-frequency radiated noise typified by power-source  
10 noise, and can read images in a high quality, and to provide an image sensor or image input system making use of such a device.

The photoelectric conversion device of the present invention comprises:

15 a first-conductivity type first semiconductor region located in a pixel region;

a second-conductivity type second semiconductor region provided in the first semiconductor region and capable of accumulating photoelectric carriers in a  
20 floating state;

a wiring for electrically connecting the second semiconductor region to a circuit element located outside the pixel region; and

a conductor provided on the wiring located inside  
25 the pixel region, via an insulator and capable of being kept at a stated potential.

The second semiconductor region may be an

island-shaped region surrounded by the first semiconductor region.

5       The second semiconductor region may also have a first part which is the island-shaped region surrounded by the first semiconductor region and a second part surrounding the first part and having a lower impurity density than the first semiconductor region.

10       The conductor may be formed integrally with a light-screening layer provided for defining the pixel region.

15       The circuit element may be an MOS transistor. Then the circuit element may comprise a resetting switch for resetting the potential of the second semiconductor region and an amplifying transistor for amplifying signals.

      To the circuit element, an accumulation circuit for accumulating a reset noise and a noise-reduction circuit for reducing the reset noise may be connected.

20       The conductor may have either width of a width smaller than the width of the wiring or a width larger than the width of the wiring.

      The photoelectric conversion device may have a terminal connected to a power source for keeping the conductor at a stated potential.

25       The photoelectric conversion device may be provided in combination with a light source and an imaging device to make up an image sensor.

In this image sensor, the photoelectric conversion device may be provided in plurality in a one-dimensional fashion or zigzag (staggered) fashion on a mounting substrate. This image sensor has a wiring for supplying a reference voltage for keeping the conductor at a stated potential.

This image sensor may be provided in combination with an original-holding means for holding an original and a control circuit for controlling the image sensor, to make up an image input system.

In this image input system, the original-holding means may comprise an original stand having a transparent top surface, or an original-carrying holding-down member. This system may also have a reference voltage source that supplies a reference voltage for keeping the conductor at a stated potential.

In the photoelectric conversion device, the conductor, which serve as a shield, may comprise a conductor extending into a substantially square opening formed in a light-screening layer to define the pixel region, and extending along the wiring located inside the pixel region. This enables the pixel region to be kept from having a large capacitance.

#### BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 illustrates a planar structure of a

photodetector in Embodiment 1 of the present invention.

Fig. 2 is a cross-sectional view along the line 2-2 in Fig. 1.

Fig. 3 shows a circuit diagram of the photoelectric conversion device according to the present invention.

Fig. 4 is a drive timing chart.

Fig. 5 is a cross-sectional view of a contact type image sensor unit in Embodiment 2 of the present invention.

Fig. 6 is a block diagram of an image input system in Embodiment 2 of the present invention.

Fig. 7 illustrates a planar structure of a photodetector in Embodiment 3 of the present invention.

Fig. 8 is a cross-sectional view along the line 8-8 in Fig. 7.

Fig. 9 is a cross-sectional view of a photoelectric conversion device according to another embodiment of the present invention, showing its pixel region and the vicinity thereof.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS (Embodiment 1)

Fig. 1 illustrates a planar structure of a photodetector as a photoelectric conversion device according to the present invention. Fig. 2 illustrates a cross-sectional structure along the line 2-2 in Fig.

1.

The photoelectric conversion device according to the present embodiment comprises:

an n<sup>-</sup>-type region 10 as a first-conductivity type  
5 first semiconductor region, located in a pixel region  
(a pixel region opening; hereinafter merely "opening")  
1;

a p<sup>+</sup>-type region 11 as a second-conductivity type  
second semiconductor region, provided in the first  
10 semiconductor region 10, where in a floating state, the  
first semiconductor region 10 may accumulate  
photoelectric carriers therein;

a wiring 5 for electrically connecting the second  
semiconductor region 11 to circuit elements 3 and 4  
15 located outside the pixel region 1; and

a conductor 6' provided on the wiring 5 located  
inside the pixel region, via an insulator 7 and kept at  
a stated potential.

The conductor 6' stands extended into the opening.

20 The present photoelectric conversion device will  
be described below in detail.

In the pixel region 1, a photodiode 2 serving as a  
photodetector is formed. The photodiode 2 is  
electrically connected to the drain of a reset MOS  
25 transistor 3 and the gate of an amplifying source  
follower MOS transistor 4 which are the circuit  
elements, through a wiring 5 formed of a first metal

layer. Also, the pixel region 1 of the photodetector corresponds to an opening roughly defined by a light-screening layer 6 formed of a second metal layer and this light-screening layer 6 is connected to a reference voltage source so as to be kept at a stated potential.

As shown in Fig. 2, the photodetector is formed by providing an anode comprising the p<sup>+</sup>-type region 11 formed in an island shape in the n<sup>-</sup>-type region 10 located in the pixel region 1. Needless to say, the conductivity type may be reversed so as to form in a p<sup>-</sup>-type region a cathode comprising an n<sup>+</sup>-type region.

Beneath the n<sup>-</sup>-type region 10, an n<sup>+</sup>-type buried region 13 is provided, which is formed on an n-type semiconductor substrate 16. This construction can be materialized by, e.g., forming an area for the n<sup>+</sup>-type buried region 13 on the n-type semiconductor substrate 16 by ion implantation followed by epitaxy to grow that region.

Beneath the light-screening layer 6 formed of a second metal layer, an n<sup>+</sup>-type region 14 is provided in a ring shape as viewed from above and in such a way that it holds the p<sup>+</sup>-type region 11 on its inside. This n<sup>+</sup>-type region 14 and the n<sup>+</sup>-type buried region 13 cooperate to form a potential barrier against photoelectric carriers generated in the pixel region. This enables effective collection of photoelectric

carriers in the  $p^+$ -type region 11 even when the  $p^+$ -type region 11 is formed in the shape of an independent island and also enables achievement of a low capacitance in the pixel region.

5           The  $p^+$ -type region 11 is further electrically connected with the wiring 5 formed of a first metal layer. Above the wiring 5, as the conductor an in-pixel-region shield 6' formed of a second metal layer is provided via an insulating layer 7, and a  
10 protective layer 12 is provided above the second metal layer.

          Here, what is most characteristic of the present invention is that at least part of the wiring 5 formed in the pixel region is covered at its upper part with a  
15 conductor 6' whose potential is fixable, i.e., the in-pixel-region shield 6'. A terminal 21 is a connecting terminal for keeping the shield 6' to a reference voltage  $V_{ref}$ .

          Usually, pixel regions of photodetectors have a  
20 structure wherein the light is incident from the outside of photoelectric conversion devices, and hence tend to be affected with noises which are radiated from the outside like the light. Especially in the case of photoelectric conversion devices used in contact type  
25 image sensors, it is common to use a mounting method in which a plurality of pixel regions are mounted on a common substrate such as a glass epoxy substrate or a



ceramic substrate and thereafter the top of the photoelectric conversion device is coated with silicone resin or the like. Hence, each pixel region of the photoelectric conversion device is not electrically  
5 shielded at its upper part, and tends to be affected with the noises radiated from the outside.

In addition, in order to improve carrier collection efficiency and also make as low as possible the capacitance of the photoelectric conversion area,  
10 i.e., the pixel region, the anode or cathode of a photodiode is formed at the middle of the opening (pixel region opening) or at a position a little deviating from the middle toward an edge of the opening. In such an instance, any conductor whose  
15 potential has been fixed is not present in the vicinity of the conductor connected electrically to the anode or cathode of a photodiode, and hence the node of the pixel region that stands floating during the accumulation of carriers acts as an antenna to tend to  
20 be affected with the noises radiated from the outside.

Now, as shown in Fig. 1, in order to make the pixel region have a low capacitance, an opening is formed in the second metal layer light-screening layer  
6 at the top of the pixel region, and some part of the  
25 top of the wiring 5 (the part extending into the pixel region) is not covered with the second metal layer 6. With regard to this part, however, the second metal

layer shield layer 6' is provided above the wiring 5.  
Hence, the greater part of the noises radiated from the  
outside is cut off and absorbed at the second metal  
layer 6', so that the first metal layer wiring 5, i.e.,  
5 the node, connected to the photodiode is less affected.

Thus, the second metal layer 6' extending into the  
opening of the light-screening layer 6 does not  
completely cover the first metal layer wiring 5 as  
shown in Fig. 2, but can sufficiently be effective.

10 Figs. 3 and 4 are a circuit diagram and a drive  
timing chart, respectively, of the photoelectric  
conversion device according to the present embodiment.

The photoelectric conversion device according to  
the present embodiment has, as shown in Fig. 3, a  
15 photodiode 2 serving as a photoelectric conversion  
means, MOS transistors 4 and 31, a switch 3 serving as  
a reset circuit of the photoelectric conversion means  
2, MOS transistors 32, 34 and 35 serving as noise  
holding means that hold the noise occurring at the time  
20 of the resetting of the photoelectric conversion means,  
and a capacitor ( $C_M$ ) 33, and has a noise-eliminating  
(noise reduction) circuit (constituents 36 to 47) which  
eliminates (reduces) noise from the signals accumulated  
by the photoelectric conversion means, after the same  
25 above resetting and by using the noise kept being held  
in the noise-holding means.

The noise-eliminating circuit has an MOS switch 36

as a noise read means which reads electric charges of the noise occurring immediately after the resetting, a capacitor ( $C_{TN}$ ) 38, an MOS switch 37 as a light-signal read means which reads light-signal electric charges  
5 after accumulation of light signals, a capacitor ( $C_{TS}$ ) 39, and a shift register 42 serving as a scanning means which successively scans the noise of the above noise read means and the light signals of the above light-signal read means. By this scanning means,  
10 signals are read out of the noise read means (36, 38) and the light-signal read means (37, 39), and simultaneously the light signals are accumulated by the above photoelectric conversion means.

This photoelectric conversion device also has a  
15 noise-holding circuit (consisting of the MOS transistors 32, 33, 34 and the capacitor 35) which holds the noise occurring immediately after the resetting, until the light signals accumulated after the same resetting are read to the light-signal read  
20 means (37, 39); buffer amplifiers 43 and 44 serving as means for outputting the noise occurring immediately after the resetting, kept being held in the noise-holding circuit, and the light signals after the same resetting; and a differential amplifier 45.

25 To the shield 6', a power-source voltage is applied through the connecting terminal 21, and the shield 6' is grounded.

Here, the MOS transistors 34 and 35 and the MOS transistors 4 and 31 form MOS source followers, respectively.

Input terminals 46 and 47 of the buffer amplifiers 43 and 44 are connected with common output wires. Circuits without the shift register 42, the buffer amplifiers 43, 44 and the differential amplifier 45 are wired correspondingly to the bits (correspondingly to the number of pixels).

In the present embodiment, the components shown in the drawing are all formed on the same semiconductor substrate.

How the photoelectric conversion device according to the present embodiment operates will be described below with reference to a timing chart shown in Fig. 4.

Upon the inputting of a start pulse SP, first the capacitors  $C_{TS}$  and  $C_{TN}$  for accumulating light signals and noise, respectively, are reset.

Subsequently, a drive pulse  $\phi_{TN}$  is applied and the noise kept being held in the capacitor  $C_M$  is read to the capacitor  $C_{TN}$ . At this time, the noise read out of the capacitor  $C_M$  is noise occurring immediately after a sensor has been reset in a previous field. After the noise has been read to the capacitor  $C_{TN}$ , a drive pulse  $\phi_{T1}$  is applied and light signals are read to the capacitor  $C_M$ . Also, a drive pulse  $\phi_{TS}$  is applied and light signals are read to the capacitor  $C_{TS}$ .

Thereafter, a drive pulse  $\phi R$  is applied to reset the sensor. Thus, the anode of the photodiode having been kept to stand floating is reset to a stated resetting reference potential. Subsequently, the drive pulse  $\phi T1$  is applied and signals occurring immediately after the sensor has been reset are read to the capacitor  $C_M$  as reset noise, where the sensor begins accumulation. Thus, the anode is made to stand floating and accumulates holes (positive holes) among photoelectric carriers.

Then, at the same time the sensor performs accumulation, the light signals and noise held in the capacitors  $C_{TS}$  and  $C_{TN}$ , respectively, are sequentially outputted to the common output wires. Thus, finally the light signals and noise are subtraction-processed (noise is subtracted from light signals) by a subtraction-processing circuit (not shown) and are outputted as net light signals.

Hence, the noise occurring upon the resetting of the sensor, which is ascribable to a reset pulse  $P1$  as shown in the timing chart, is held in the capacitor  $C_M$  by a pulse  $P2$  during the accumulation, and is inputted to the capacitor  $C_{TN}$  by a pulse  $P3$  before the light signals are read. Thus, the results of subtraction of the noise against the same sensor from the light signals inputted by pulses  $P4$  and  $P5$  can be outputted as net light signals, and hence the sensor reset noise

can completely be eliminated.

The noise-eliminating circuit is by no means limited to the embodiment described above. For example, any known clamping circuit may also be used.

5        The photoelectric conversion device having the structure as shown in Figs. 1 to 4 and a photoelectric conversion device of a comparative example having the same structure as the above except that the shield 6' is omitted were put to an experiment to compare their effects against radiated noise.

10       Here, the noise source was in a frequency of 50 Hz. The noise having a frequency component of 50 Hz outputted from each photoelectric conversion device was measured, setting the electric field between the photoelectric conversion device and that of the noise source as parameters. Results obtained are shown in Table 2.

Table 2

20	Noise electric field	Present embodiment (mV rms)	Comparative example (mV rms)
	0 V	0.32	0.32
	50 V	0.36	1.75
25	100 V	0.60	3.61

As can be seen from the foregoing, a remarkable effect is seen under a noise electric field intensity

of 50V. Also, under a noise electric field intensity of 100V, the present embodiment brings the noise to a level of about 1/6 of that of the comparative example.

As shown in the foregoing, shielding only the  
5 necessary minimum portion in the opening of the photoelectric conversion device makes it possible to keep the pixel region from having a large capacitance and also to attain a sufficient shield effect against radiated noise. Needless to say, when the pixel region  
10 has room for capacitance in designing, the wiring 5 to which the photodiode is connected and the top of its node may completely be covered with the second metal layer conductor (shield) 6' having a stated potential.

In the present embodiment, the wiring 5 and the  
15 shield 6' are constituted of metal layers such as aluminum layers, and hence substantially has the function of light screening. The part called the pixel region or the opening in the present invention is a square opening part defined by corners a, b, c and d in  
20 Fig. 1. Also, this shield 6' extends into the square opening part from an edge. Thus, the opening is kept not to have a large capacitance and a small opening ratio.

(Embodiment 2)

25 The present embodiment shows an example in which the photoelectric conversion device shown in Embodiment 1 is used to construct a contact type image sensor and

the contact type image sensor is further applied to an image input system.

Fig. 5 is a cross-sectional view of a contact type image sensor unit constructed using the photoelectric conversion device shown in Embodiment 1.

As shown in Fig. 5, at the top of a housing 104, a transparent glass plate 105 as an original-holding means is provided which is to come in contact with the face of an original. Also, an LED light source 109 is provided inside the housing 104 in the state it is attached to an LED mounting substrate 110; the light source 109 being provided at an angle so preset that emitted light 111 reflects from the face of an original placed in contact with the top surface of the transparent glass sheet 105.

A plurality of photoelectric conversion device 100 IC chips are arranged in a row or in a zigzag fashion on a sensor mounting substrate 101. A lens array 108 which is an imaging device for forming an image on the photoelectric conversion device 100 is also provided inside the housing 104.

The photoelectric conversion device 100 is electrically connected to a desired circuit of the sensor mounting substrate 101, and is covered with a protective member 103. Here, as the protective member, usable are light-transmitting insulating materials such as silicone resin, epoxy resin and polyimide resin.



The sensor mounting substrate 101 is provided with wiring 22 such as a power-source line and a grounding line, for applying a reference potential to the connecting terminal 21 (Fig. 1).

5           The sensor mounting substrate 101 is also supported on a bottom plate 106 via a rubber sheet 107, the bottom plate 106 being engaged in the housing 104. The housing 104 is also provided with a connector for connecting the device to the exterior, e.g., to a  
10 scanner main body or a facsimile main body. Through the connector, electric power, control signals and so forth are inputted or outputted.

          In the case of an image sensor unit which reads color images, the LED light source 109 has at least  
15 three color LEDs where, e.g., red, green and blue LEDs are assembled. At the time the LED light source 109 is emitting red light only, the photoelectric conversion device 100 is driven to read red-color information. Next, only the green-color LED is put on to read  
20 green-color information. Finally, only the blue-color LED is put on to read blue-color information. A color image can be read by combining these information.

          Here, the unit shown in Fig. 5 has a structure wherein the protective member 103, lens array 108 and  
25 transparent glass sheet 105 are provided above the opening of the photoelectric conversion device 100. Since these are commonly members having insulating

properties, they can not have any shielding effect against the radiated noise occurring on the outside, and the noise can easily reach the opening part of the photoelectric conversion device 100. However, the use  
5 of the photoelectric conversion device of the present invention makes it possible to materialize a contact type image sensor that may hardly be affected with the noise.

Fig. 6 shows an example of an image input system  
10 employing the image sensor according to the present invention.

This image input system, 200, is constituted chiefly of a sensor drive means 201 for electrically driving the photoelectric conversion device and light  
15 source set in a contact type image sensor unit 205, a signal processing means 202 for processing signals outputted from the contact type image sensor unit 205, a sensor position control means 203 for controlling the position of secondary scanning of the contact type  
20 image sensor unit 205, and a power-source means 204 for supplying power-source voltage to the above electric component parts, and also is so set up that its operation is controlled by a CPU serving as a control circuit. Also, e.g., lines 23 serve as power-source  
25 lines (or grounded lines) through which a reference voltage is applied to keep the shield 6' being held at a reference potential. The reference voltage is

supplied from the power-source means 204.

In the present embodiment, the sensor position control means 203 may be constituted of, e.g., mechanism component parts such as a stepping motor, a shaft and a belt which are used in combination. In the present embodiment, an example is shown in which the original is placed on an original stand having a glass-plate top and the image sensor is moved in the secondary-scanning direction to perform scanning. Also usable is an image read system of a type in which the image sensor is set stationary and the original is held between the transparent glass plate 105 and an original-holding-down member (not shown) and moved in the secondary-scanning direction, i.e., what is called a sheet-feeding type. In this instance, the original-holding means is constituted of the transparent glass plate 105 and the original-holding-down member.

Here, using the photoelectric conversion devices of the present invention and of the comparative example, described in Embodiment 1, the contact type image sensors were set up and were each applied in the above image input system to read images. As a result, in images read by the contact type image sensor of the comparative example, periodic lateral stripes appeared in the direction perpendicular to the secondary-scanning direction. However, in images read

by the contact type image sensor of the present invention, such stripe-like difficulties did not occur.

Stated more specifically, in the image input system shown in Fig. 6, the use of the photoelectric conversion device of the present invention has solved the problem that the image quality deteriorates because of the noise radiated from the power-source means 204 or sensor position control means 203 and the low-frequency noise radiated from the outside of the image input system 200.

In other words, according to the present embodiment, a large margin of designing can be taken for measures against the radiated noises occurring in power sources and drive sources used in image read instruments. Hence, an image input system which can obtain high-quality images can be materialized at a low cost.

(Embodiment 3)

Fig. 7 illustrates a planar structure of a photodetector as a photoelectric conversion device according to another embodiment of the present invention. Fig. 8 shows a cross-sectional structure along the line 8-8 in Fig. 7.

The present embodiment shows an example of a photodetector embodied differently from that in Embodiment 1. As shown in Figs. 7 and 8, a p-type region 18 of a buried photodiode serving as the

photodetector is formed in a pixel region 1. A  $p^+$ -type region 11 is formed in the p-type region 18 of this photodiode, which is electrically connected to the drain of a reset MOS transistor 3 and the gate of an  
5 amplifying source follower MOS transistor 4 through a wiring 5 formed of a first metal layer. Also, the pixel region 1 of the photodetector corresponds to an opening defined by a light-screening layer 6 formed of a second metal layer and this light-screening layer 6  
10 is connected to a power source so as to be kept at a stated reference potential.

As shown in Fig. 8, the p-type region 18 is formed in the pixel region 1 of an n-type region 19 provided on a p-type semiconductor substrate 17. Also, in the  
15 p-type region 18 in the n-type region 19, a  $p^+$ -type region 11 is provided in an island shape.

On the surface of the p-type region 18, an  $n^+$ -type surface region 20 is provided which separates the p-type region 18 from the underside of an insulating  
20 layer lying above that region. This region 20 stands adjoining to the n-type region 19 at the periphery of the pixel region 1, and is electrically connected thereto.

Thus, a photodiode is formed by the p-n junction  
25 between the p-type region 18, the n-type region 19 and the  $n^+$ -type surface region 20. Among photoelectric carriers formed by photoelectric conversion in the

photodiode, holes are collected in the p<sup>+</sup>-type region 11 to cause a change in potential of the wiring 5 formed of a first metal layer.

5 The p<sup>+</sup>-type region 11 is further electrically connected with the wiring 5 formed of a first metal layer. Above the wiring 5, an in-pixel-region shield 6' formed of a second metal layer is provided via an insulating layer 7, and a protective layer 12 is provided above the second metal layer.

10 Here, in the photodetector shown in Fig. 8, in order to lower the capacitance of the device, the n-type region 19 and the n<sup>+</sup>-type surface region 20 have their own impurity densities so that the p-type region 18 held between them is depleted during the operation  
15 of the photodetector.

Using the photoelectric conversion device shown in Figs. 7 and 8 and a photoelectric conversion device of a comparative example in which the shield 6' is not provided, circuits were constructed as shown in Fig. 3,  
20 to compare their effects against radiated noises. As a result, the same results as in the case of Embodiment 1 were obtained.

Fig. 9 is a cross-sectional view of a photoelectric conversion device according to still  
25 another embodiment of the present invention, showing its pixel region and the vicinity thereof.

This device has a construction different from that

in Fig. 1 in that the shield 6' has a width W1 a little larger than a width W2 of the wiring 5. Of course, W1 may be equal to W2.

As the first and second metal layers 5, 6 and 6' used in the present invention, usable are single layers or multiple layers comprised of a metal selected from the group consisting of gold, aluminum, copper, molybdenum, tungsten, titanium and tantalum. An alloy composed chiefly of some of the above metals may also be used, as exemplified by Al-Cu, Al-Si-Cu, Ti-W or W-Mo.

The insulating layer 7 used in the present invention may be formed of a insulator such as silicon oxide or silicon oxide nitride, or an insulator with a low dielectric constant, such as fluorine-containing silicon oxide or an organic insulator, which is formed by CVD (chemical vapor deposition) or coating.

The n<sup>+</sup>-type region 14 may also be replaced with an insulator or a p-type device-separating region.

The pixel region is an opening which may be in the form of a square or polygon having a side of from 1  $\mu\text{m}$  to 80  $\mu\text{m}$ . The shield 6' may have a width of from 0.18  $\mu\text{m}$  to 20  $\mu\text{m}$ .

In all Embodiments described above, the p<sup>+</sup>-type region 11 is provided in an island shape in the n<sup>-</sup>-type region 10 to form the photodiode. Alternatively, their conductivity type may all be reversed so that the

n-type region is provided in the p-type region to form a photodiode.

5       The p<sup>+</sup>-type region 11 also need not necessarily have a square island shape. For example, it may have the shape of a rectangle, a cross or a ring, any of which is applicable, and may be so designed as to attain optimum characteristics, taking account of carrier collection performance and photodetector capacitance.

10       As described above, the constitution of the present invention makes it possible to materialize a photoelectric conversion device that may hardly be affected with radiated noises, in particular, a low-frequency radiated noise typified by power-source  
15       noise, and can read images in a high quality. The application of the present invention makes it possible to achieve cost reduction of photoelectric conversion devices, contact type image sensors and image input systems and at the same time to achieve high-quality  
20       imaging.



WHAT IS CLAIMED IS:

1. A photoelectric conversion device comprising:  
a first-conductivity type first semiconductor  
region located in a pixel region;

5 a second-conductivity type second semiconductor  
region provided in the first semiconductor region and  
capable of accumulating photoelectric carriers in a  
floating state;

10 a wiring for electrically connecting the second  
semiconductor region to a circuit element located  
outside the pixel region; and

a conductor provided on the wiring located inside  
the pixel region, via an insulator and capable of being  
kept at a stated potential.

15

2. The photoelectric conversion device according  
to claim 1, wherein the second semiconductor region is  
an island-shaped region surrounded by the first  
semiconductor region.

20

3. The photoelectric conversion device according  
to claim 1, wherein the second semiconductor region has  
a first part which is the island-shaped region  
surrounded by the first semiconductor region and a  
25 second part surrounding the first part and having a  
lower impurity density than the first semiconductor  
region.

4. The photoelectric conversion device according to claim 1, wherein the conductor is formed integrally with a light-screening layer provided for defining the pixel region.

5

5. The photoelectric conversion device according to claim 1, wherein the circuit element is an MOS transistor.

10

6. The photoelectric conversion device according to claim 1, wherein the circuit element comprises a resetting switch for resetting the potential of the second semiconductor region and an amplifying transistor for amplifying signals.

15

7. The photoelectric conversion device according to claim 1, wherein, to the circuit element, an accumulation circuit for accumulating a reset noise and a noise-reduction circuit for reducing the reset noise is connected.

20

8. The photoelectric conversion device according to claim 1, wherein the conductor have a width smaller than the width of the wiring.

25

9. The photoelectric conversion device according to claim 1, wherein the conductor have a width larger

than the width of the wiring.

10. The photoelectric conversion device according  
to claim 1, which further comprises a terminal  
5 connected to a power source for keeping the conductor  
at a stated potential.

11. An image sensor comprising:  
a light source;  
10 an imaging device; and  
the photoelectric conversion device according to  
claim 1.

12. The image sensor according to claim 11,  
15 wherein the photoelectric conversion device is provided  
in plurality in a one-dimensional fashion or staggered  
fashion on a mounting substrate.

13. The image sensor according to claim 11, which  
20 further comprises a wiring for supplying a reference  
voltage for keeping the conductor at a stated  
potential.

14. An image input system comprising:  
25 an original-holding means for holding an original;  
the image sensor according to claim 11; and  
a control circuit for controlling the image

sensor.

15. The image input system according to claim 14,  
wherein the original-holding means is an original stand  
5 having a transparent top surface, or an  
original-carrying holding-down member.

16. The image input system according to claim 14,  
which further comprises a reference voltage source that  
10 supplies a reference voltage for keeping the conductor  
at a stated potential.

17. The photoelectric conversion device according  
to any one of claims 1 to 3, wherein the conductor  
15 extends into a substantially square opening formed in a  
light-screening layer to define the pixel region, and  
along the wiring located inside the pixel region.

ABSTRACT OF THE DISCLOSURE

In a photoelectric conversion device comprising a first-conductivity type first semiconductor region located in a pixel region, a second-conductivity type  
5 second semiconductor region provided in the first semiconductor region, and a wiring for electrically connecting the second semiconductor region to a circuit element located outside the pixel region, a shield is provided on the light-incident side of the wiring, via  
10 an insulator in such a way that it covers at least part of the wiring and also the shield comprises a conductor whose potential stands fixed.

This photoelectric conversion device may hardly be affected with low-frequency radiated noises as typified  
15 by power-source noise.

**FIG. 1**

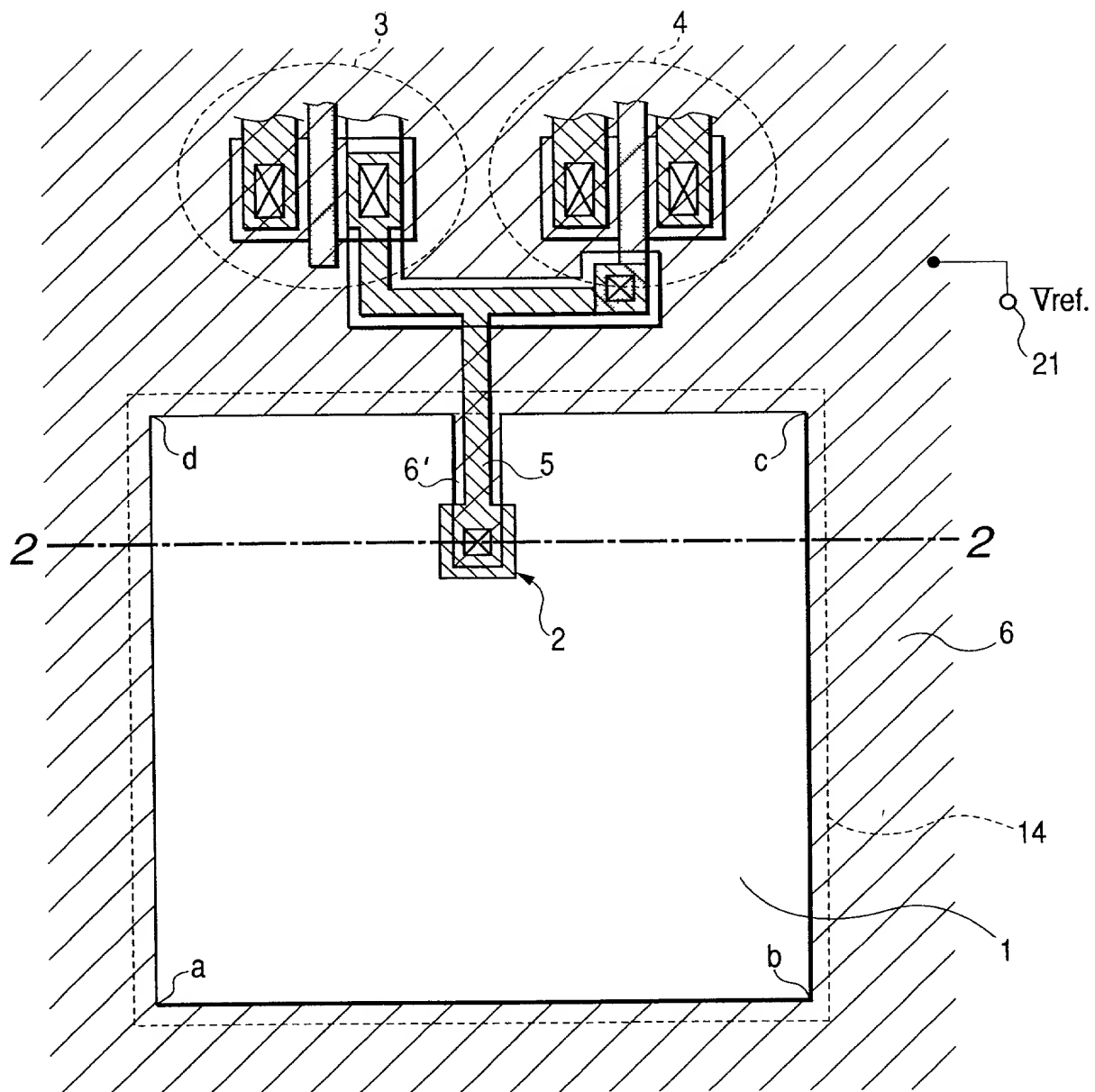


FIG. 2

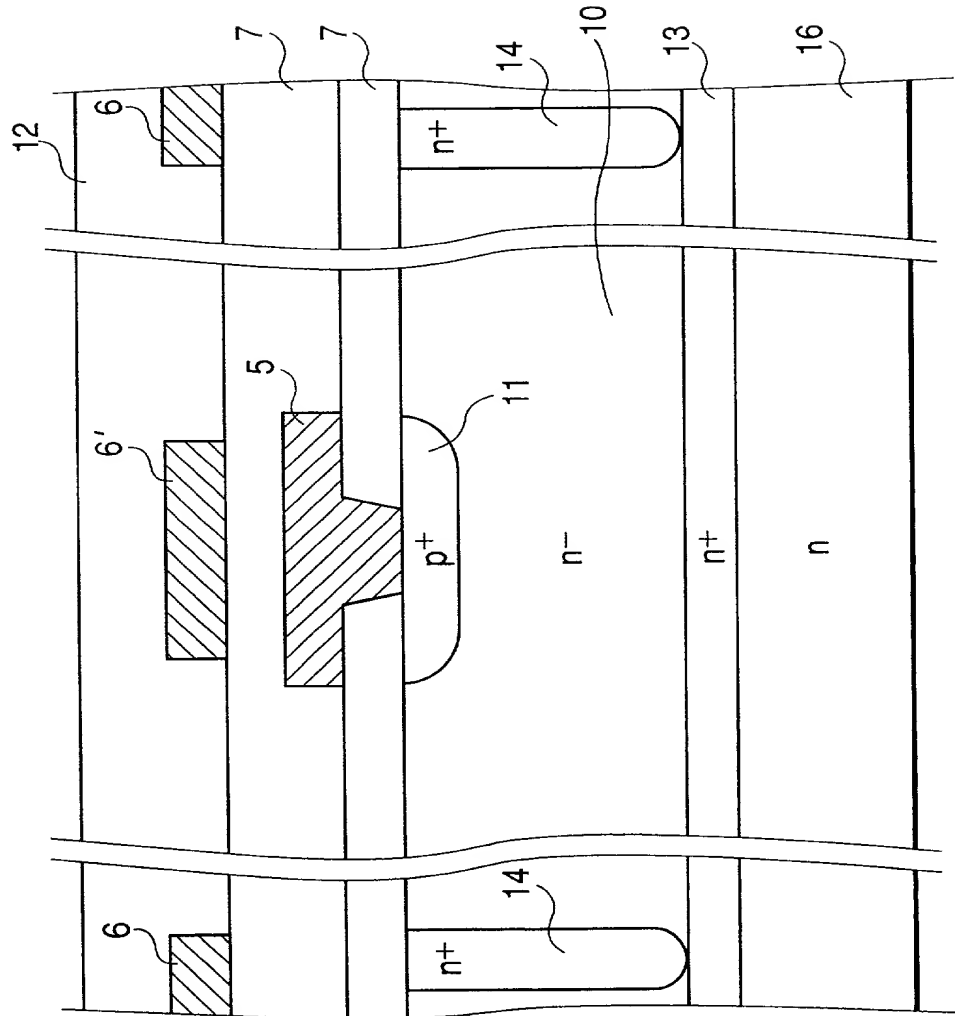


FIG. 3

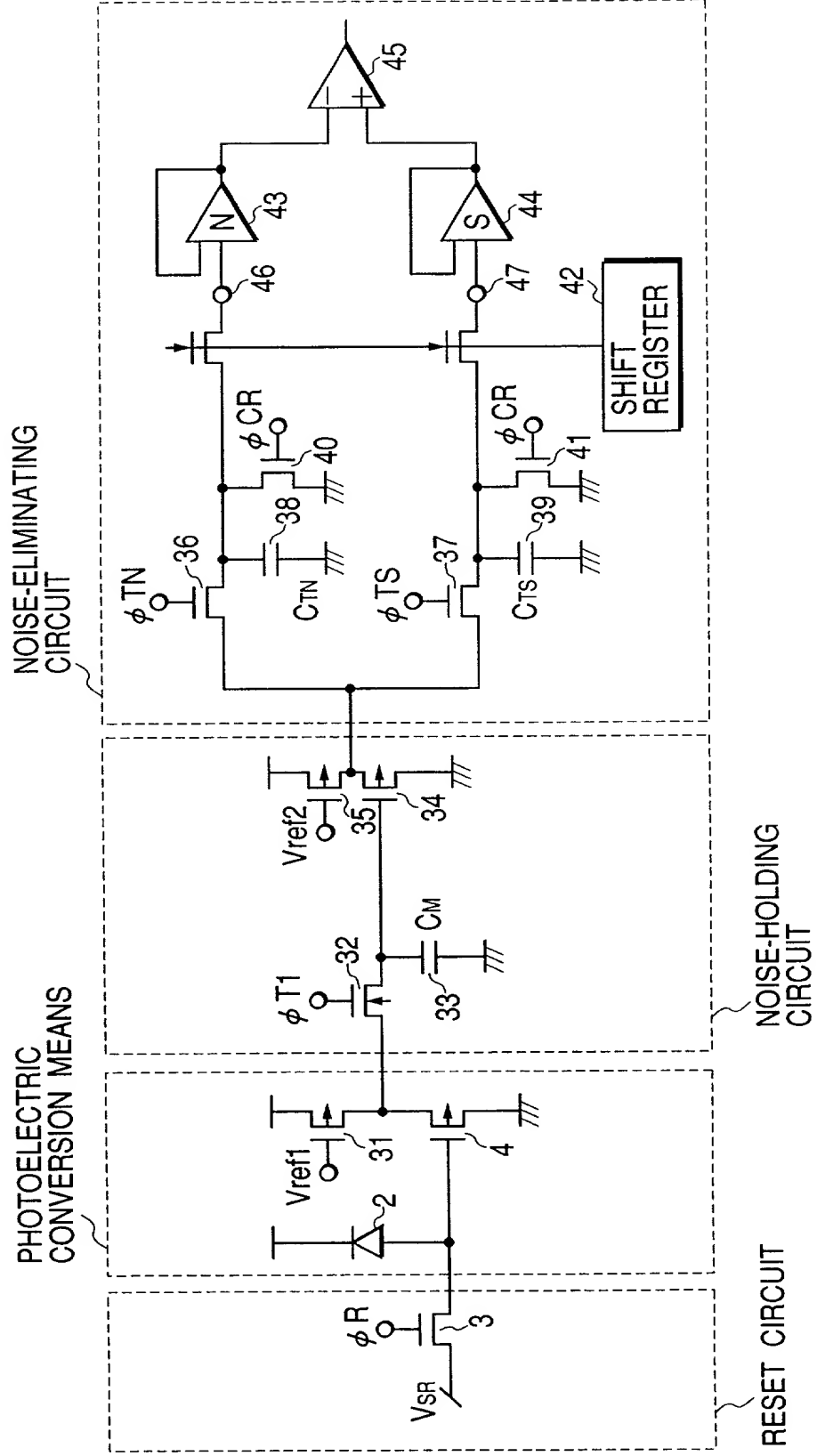




FIG. 4

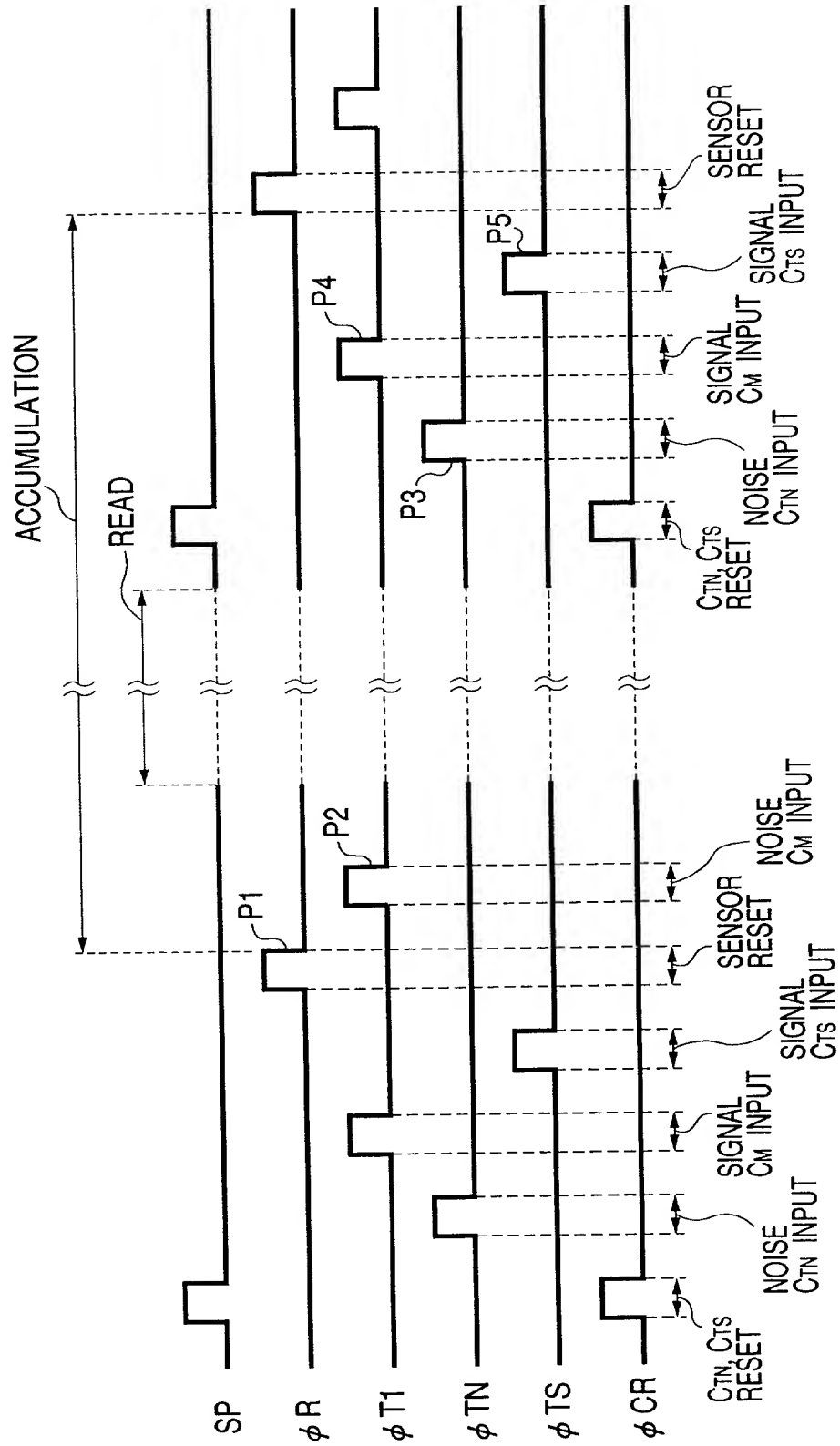


FIG. 5

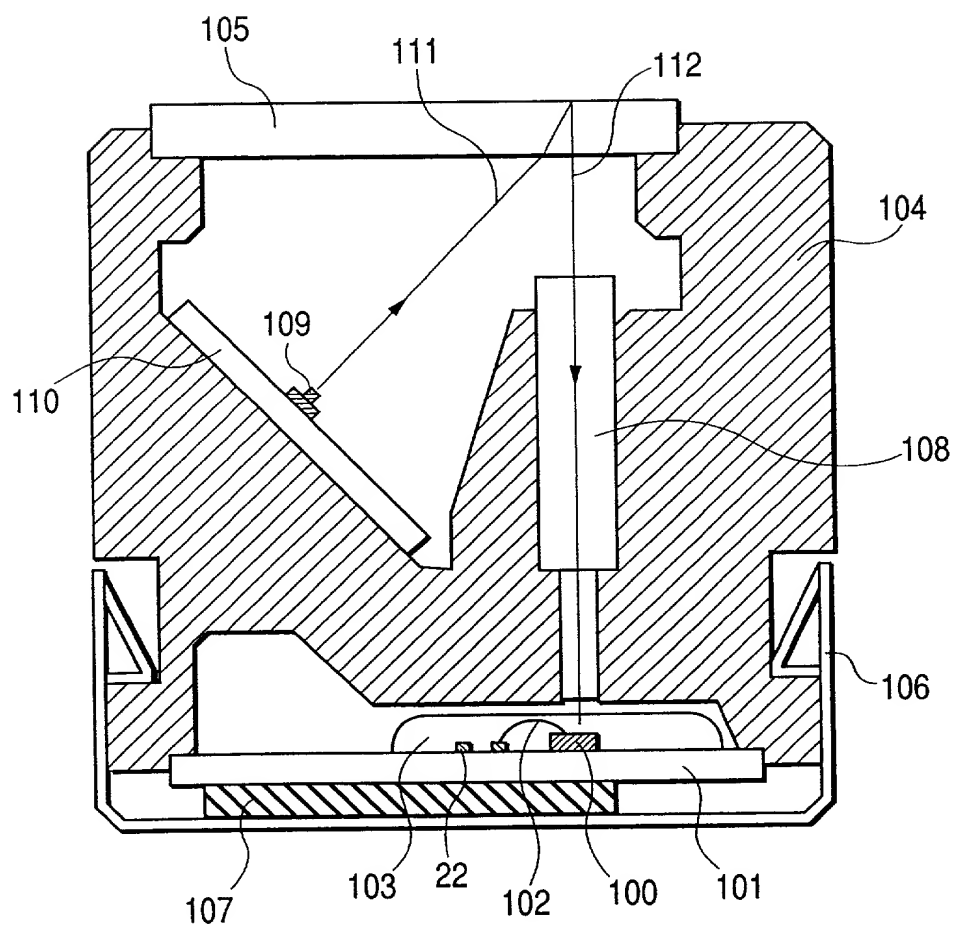


FIG. 6

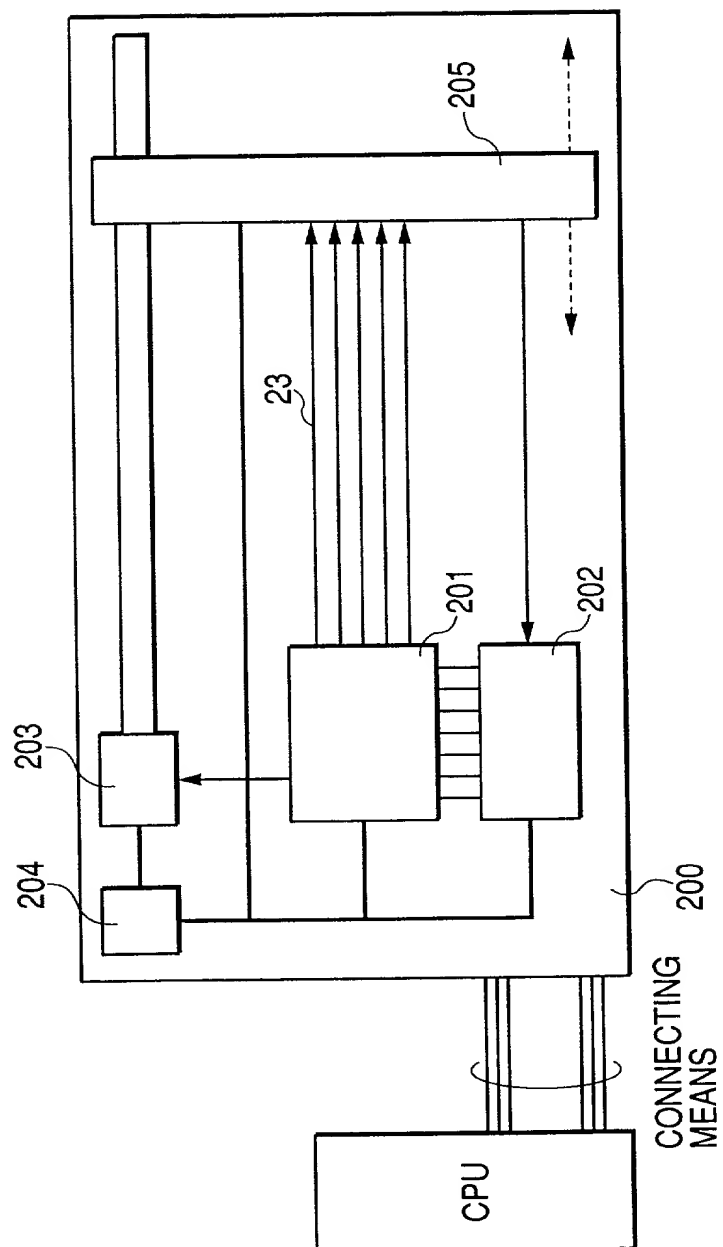


FIG. 7

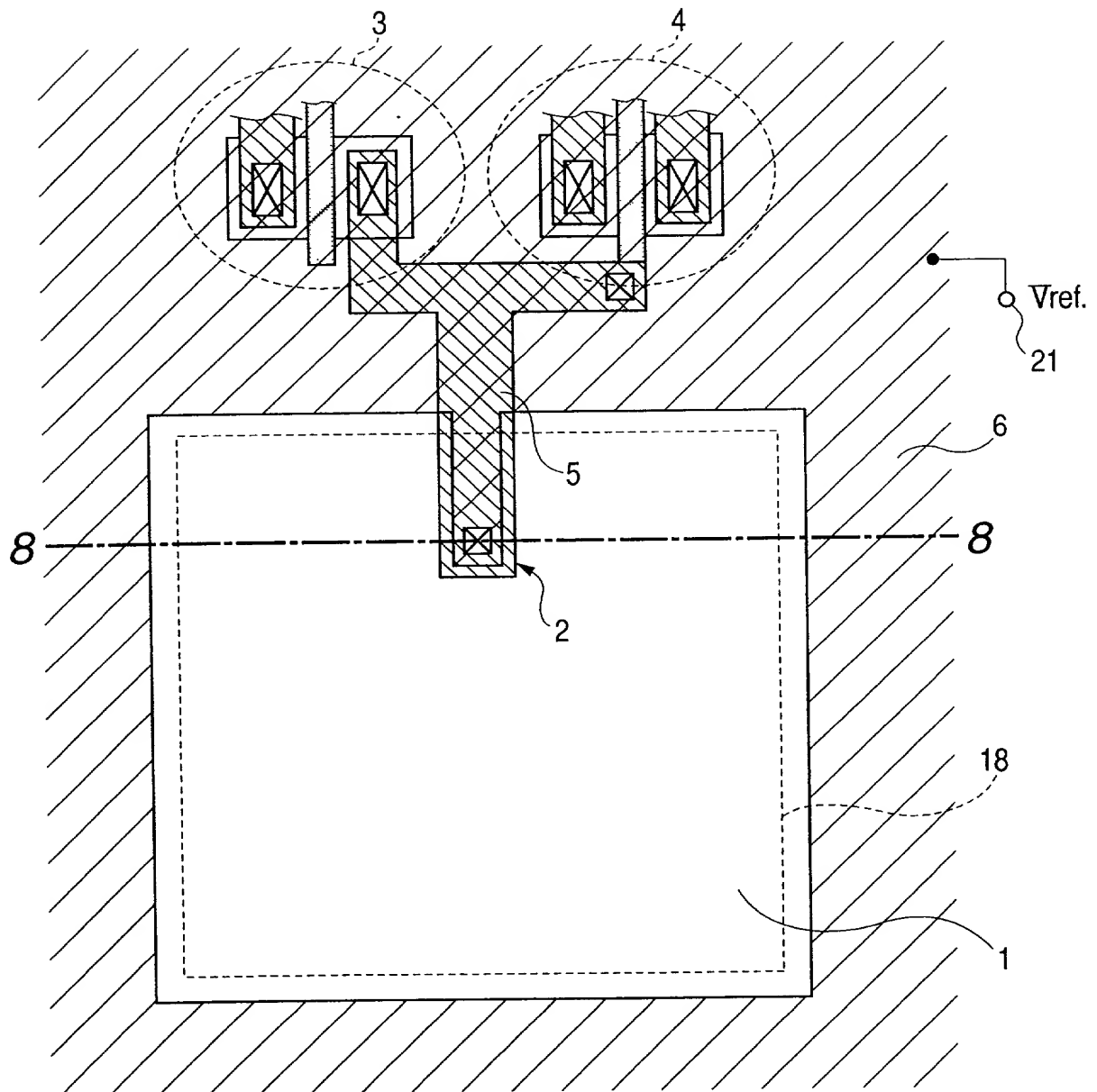
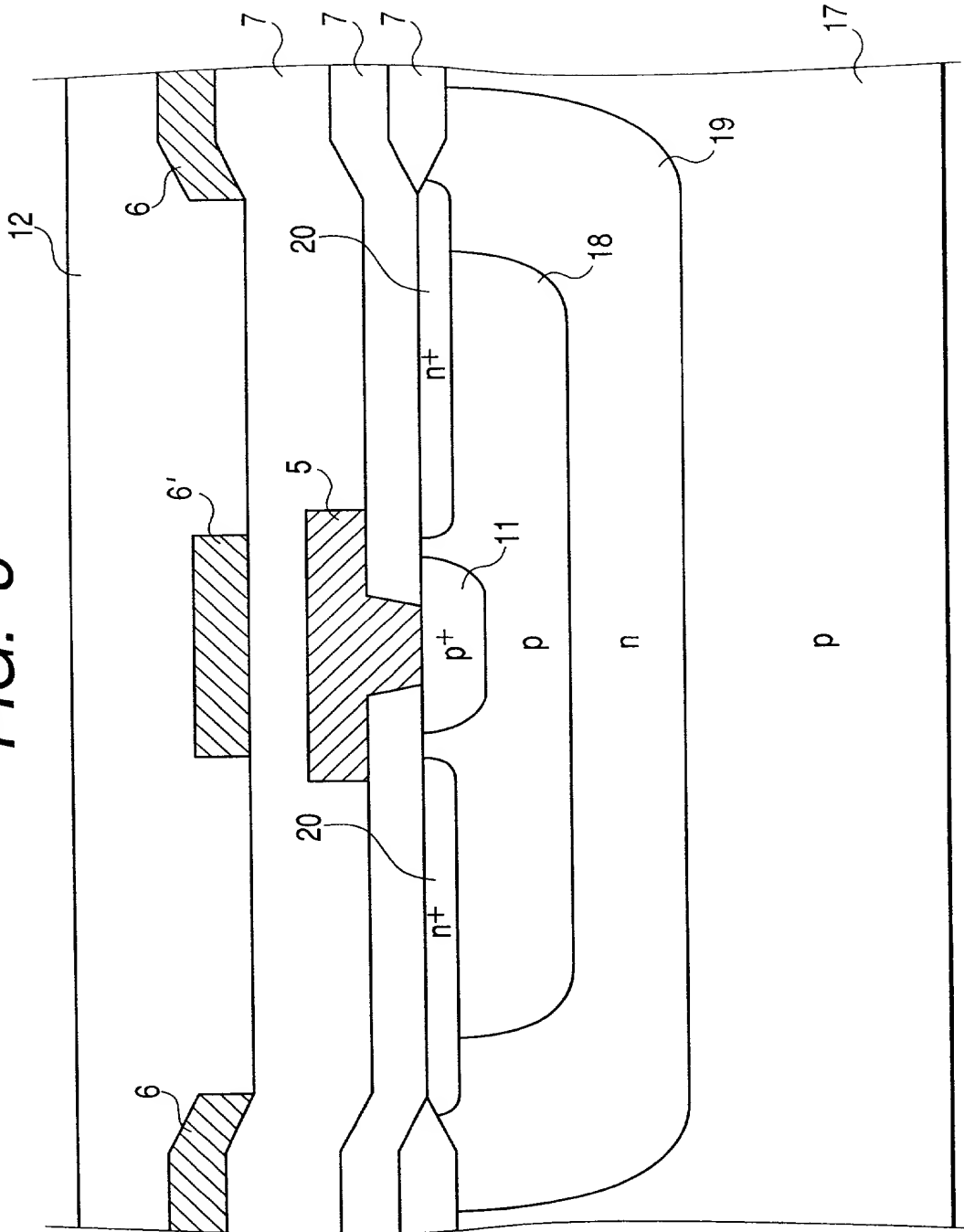
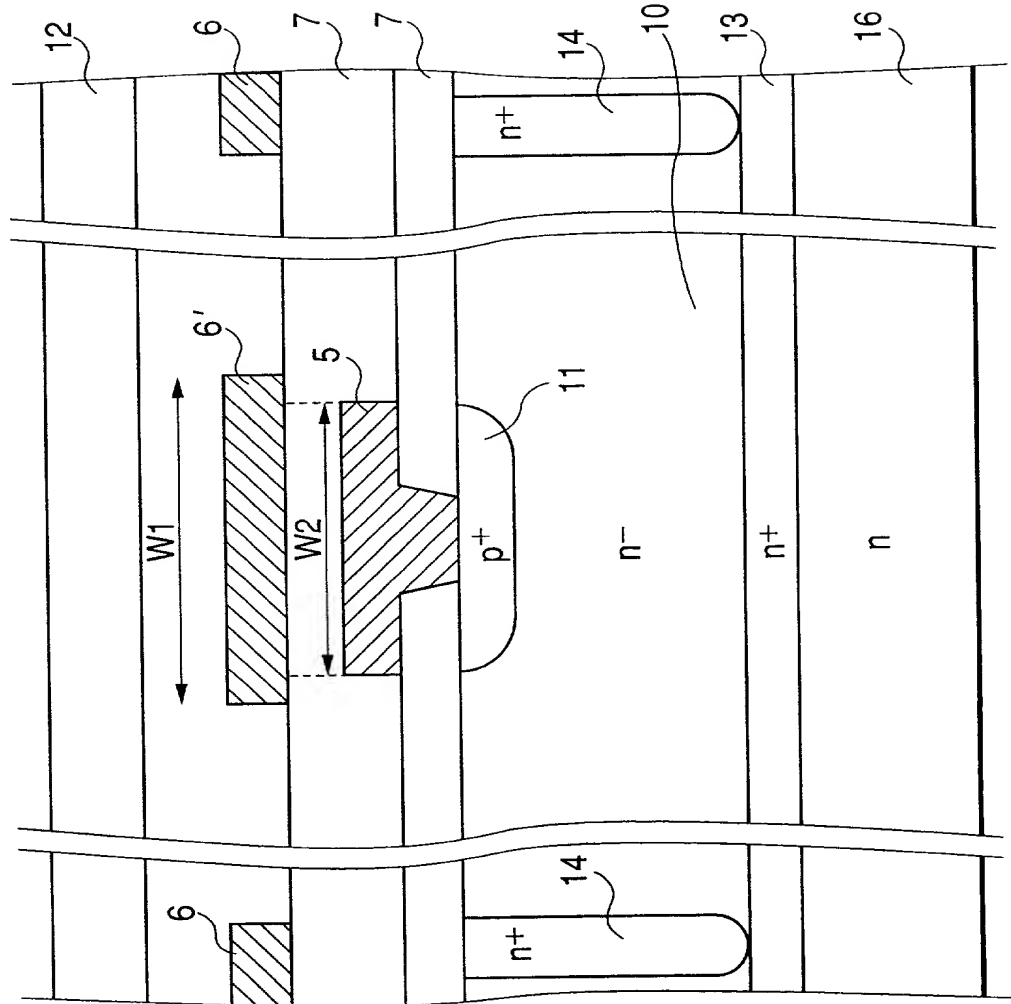


FIG. 8



**FIG. 9**



COMBINED DECLARATION AND POWER OF ATTORNEY  
FOR PATENT APPLICATION

(Page 1)

As a below named inventor, I hereby declare that

My residence, post office address and citizenship are as stated below next to my name

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled  
PHOTOELECTRIC CONVERSION DEVICE, AND IMAGE SENSOR AND IMAGE INPUT  
SYSTEM MAKING USE OF THE SAME

the specification of which ☒ is attached hereto ☐ was filed on \_\_\_\_\_ as United States  
Application No. or PCT International Application No. \_\_\_\_\_  
and was amended on \_\_\_\_\_ (if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended  
by any amendment referred to above

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR §1.56

I hereby claim foreign priority benefits under 35 U.S.C. §119(a)-(d) or §365(b), of any foreign application(s) for patent or inventor's  
certificate, or §365(a) of any PCT international application which designates at least one country other than the United States, listed below  
and have also identified below any foreign application for patent or inventor's certificate, or PCT international application having a filing date  
before that of the application on which priority is claimed.

<u>Country</u>	<u>Application No.</u>	<u>Filed (Day/Mo/Yr)</u>	<u>(Yes/No)</u> <u>Priority Claimed</u>
JAPAN	11-049065	25 February 1999	YES

I hereby claim the benefit under 35 U.S.C. §120 of any United States application(s), or §365(c) of any PCT international application  
designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the  
prior United States or PCT international application in the manner provided by the first paragraph of 35 U.S.C. §112, I acknowledge the duty  
to disclose information which is material to patentability as defined in 37 C.F.R. §1.56 which became available between the filing date of the  
prior application and the national or PCT international filing date of this application.

<u>Application No.</u>	<u>Filed (Day/Mo/Yr)</u>	<u>Status (Patented, Pending, Abandoned)</u>
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I hereby appoint the practitioners associated with the firm and Customer Number provided below to prosecute this application and  
to transact all business in the Patent and Trademark Office connected therewith, and direct that all correspondence be addressed to the address  
associated with that Customer Number.

**FITZPATRICK, CELLA, HARPER & SCINTO**  
Customer Number: 05514

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief  
are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are  
punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements  
may jeopardize the validity of the application or any patent issued thereon.

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